Demonstration for Integrated Model-Based Diagnostic, Test, Maintenance and Sustainment Solution

June 23, 2021

This demonstration and the underlying integrated solution were developed jointly by the following companies:

- DSI International (eXpress, RTAT, Workbench, STAGE)
- National Instruments (TestStand ATML Toolkit)
- Reston Software (ATML Pad)
- Spherea Technology (newWaveX, Diagnostic Aid)

Note: This diagram represents the process flow of the reference demonstration. It is not intended to describe the entirety of functional and interfacing capabilities of the software tools. These capabilities support many other process flows. Please contact the tool vendors for detailed information:

DSI International Products

TestStand ATML Toolkit

newWaveX

ATML is the "Automatic Test Markup Language", standards IEEE 1671 and IEEE 1636.1

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importer.

Stereotypes from the express profile are

eXpress-specific items are created (ex.

"eXpress Output Functions")

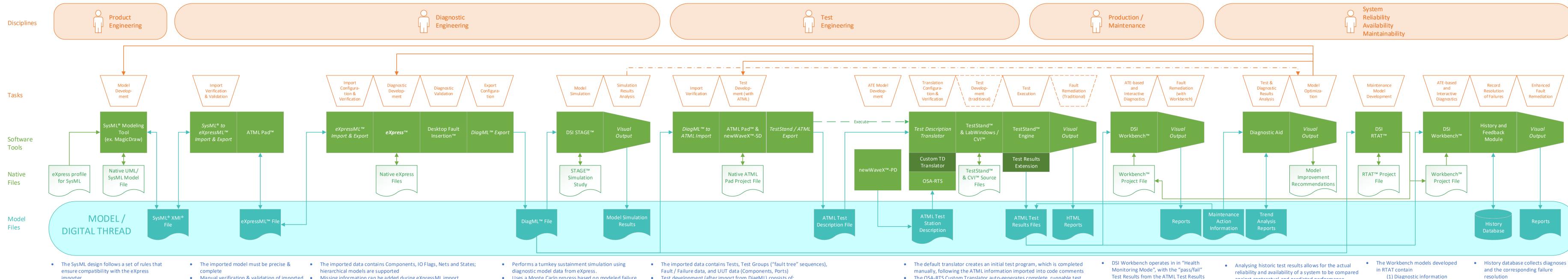
assigned to items that are imported into

eXpress (ex. Blocks, Ports, Connectors, States).

The properties defined in stereotypes are set.

data

Automatic validation against the eXpressML



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- (2) Describing Tests
- (3) Auto-generating a Diagnostic Study

Diagnostic development (after import from SysML) consists of

(1) Specifying failure data for Components

Manual verification & validation of imported
 Missing information can be added during eXpressML import

- (4) Evaluating performance of fault
- sustainment-related behavior over time-including critical Signal-based, UUT-oriented models make possible the targeting of

- rates and distribution functions. Supports "cocktails" of diagnostics, prognostics &

- detection & isolation
- (5) Iterating until requirements are met
- (6) Validate diagnostic procedures, to ensure suitability for deployment

- Provides a mechanism for validating model accuracy
- - failures, false alarms, RAMS-T metrics, prognostic

- Uses a Monte Carlo process based on modeled failure
 Test development (after import from DiagML) consists of:
- scheduled maintenance. (2) Describing commands used to control UUT state
- during testing based on simulated performance • 130+ scripted calculations analyze different aspects of

- (1) Describing stimuli, measurements, and limits
 - (2) Describing Test behavior using sequences of Operations
 - (3) Describing preconditions for Tests and Test Groups
 - different ATE platforms and test languages / executives
- The OSA-RTS Custom Translator auto-generates complete, runnable test programs through:

 - (2) Resource allocation (automatic, manual, or mixed) requires
 - signal-oriented models of ATE resources and their capabilities (ATML Test Station Description) (3) Switch path calculation (manual or automatic; static or dynamic)
- Test Results from the ATML Test Results documents. When any test fails, it displays a red horizontal bar and highlights the
- (1) Translation of signal-based ATML Operations into calls to a hardware suspect items. control API (commands, instrument drivers, "Hardware Abstraction From this point, the technician can
 - continue with any additional testing as needed to isolate to smaller fault groups o to a single component (depending on diagnostic design)
- reliability and availability of a system to be compared
- against contractual and predicted performance. Changing our models will adjust past predictions,
 - aligning the new model predictions with current performance will represent a more accurate model of
- Changes to the model will reflect true performance and can be used to address better designs and updates to meet original or adapted system goals
- (1) Diagnostic information
- (2) System topology diagram (3) Optionally, system images mapped to topology through

for mapping to ATML Test

- overlays
- (4) Messages & callouts
- (5) Configuration information
- eXpress.
- in DSI Workbench as an empirical complement to the engineeringbased diagnostics developed in

history database can be displayed

Historical Resolutions from the